

# Bridging Supply Chain Gap for Exempt High-Reliability OEM's

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RoHS exempt high reliability OEMs breathed a sign of relief for not having to go through the grind of revising their processes and material to be RoHS compliant. However, this was short lived because of supply chain disconnects in the availability of non-RoHS devices. Consumption, in terms of unit volume for Sn/Pb, is small compared to the volume going into the builds of Pb free consumer and commercial product. Many device manufacturers are discontinuing the Sn/Pb option on many part numbers (P/N) when unit volumes fall below a certain threshold.

Bills of materials are being transitioned to obsolete and legacy parts outside the control of the OEMs and at a rapid pace. The life cycle for a military product generally takes over two years for the design and initial deployment, followed by a production life cycle of over 10 years and a repair/warranty cycle of 20 plus years. A redesign to include an alternate part number is no easy task due to redesign review, validation and reliability testing.

In addition, exempt OEMs are exposed to other problems caused by some manufacturers not changing P/Ns once the Sn/Pb is obsolete. The end result too often is mixed reels of RoHS and non-RoHS product. Unfortunately, exempt OEMs are many times left with only one choice and that is Pb-free components. This is clearly not optimal due to some of the reliability concerns associated with Pb-free components. Reflow profiles, thermal stress, MSL, tin whiskers, tin pests, brittleness, voids and thermal mismatch are some of the reliability problems that can't be ignored and can't be managed in the absence of the specific Sn/Pb component.

## Test Plan of various Pb free Alloy Compositions

- Perform a limited sample size thermal cycle "screening" test covering different alloys
- Use existing test board design and component (Acceleration Factors Test)
- Single thermal cycle (0-100C, 30 minute dwell)
- Single soldering alloy: SAC 305

## Pb free Alloy Selection

Packages of 9 different solder alloys were received

- SAC405
- SnAg
- Sn100C
- Sn1.2Ag0.5Cu0.005Ni
- SAC205+Ni
- SAC310 +1%Cu
- SAC105
- SAC108
- SACX

Each Alloy came into 2 different groups

- 1) Fully populated components
- 2) Depopulated components

**Table 1 Test Results**

Alloy & Pkg Configuration	PCT Fails	Test Status (893 cycles)
SAC405F	56%	
SAC405D	0%	
SnAgF	6%	
SnAgD	25%	
Sn100CF	31%	
Sn100CD	100%	removed from chamber
LF35F	44%	
LF35D	88%	removed from chamber
SAC205NF,	19%	
SAC205ND	88%	removed from chamber
SAC310F	50%	
SAC310D	100%	removed from chamber
SAC105F	19%	
SAC105D	88%	removed from chamber
SAC108F	63%	
SAC108D	88%	removed from chamber
SACXF	81%	removed from chamber
SACXD	100%	removed from chamber

Test concluded a high PCT fail which resulted in removal of test samples & termination of test.

#### **Alternative to Pb Free Alloy**

There is hope in bridging this gap. Proprietary processes have been developed for stripping Pb-free BGA's mitigating inter-metallic layers & attaching Sn/Pb spheres utilizing controlled thermal profiles. Similar reliable processes have been demonstrated for the conversion of Pb free parts such as QFP, TSOP, PLCC, resistors & other types to Sn/Pb. This conversion option to Sn/Pb gives device manufacturers and high reliability users real alternatives on how to deal with the availability of Sn/Pb parts and be able to put the Sn/Pb proven reliability back into production builds.

The conversion process for BGA's is more complex to control compared to other device types. RoHS initiatives to remove lead from electronic equipment have resulted in challenges for manufacturers. Due to known reliability issues with tin solder alloys companies building high-reliability equipment must approach lead-free soldering with care. The technique considered for BGA devices involves the removal of lead-free solder and replacement by tin-lead spheres.

#### **Reliability Concerns with the Reballing Process**

##### **Additional Thermal Exposure (Silicon to Package Interface)**

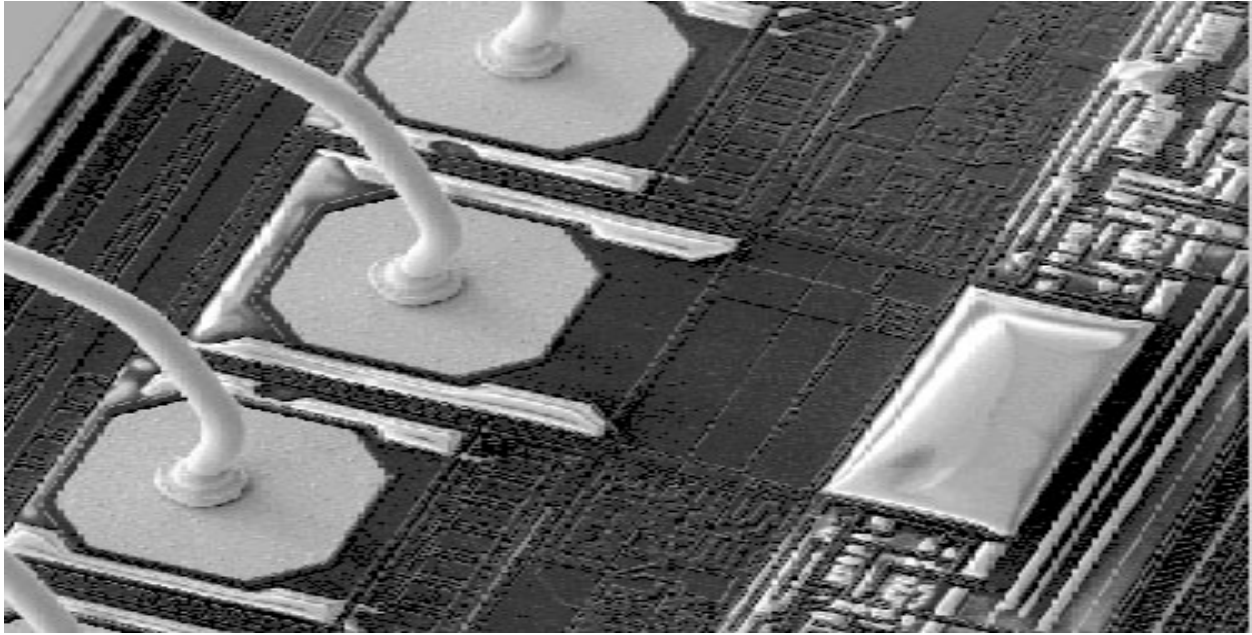


Figure 1 Wire Bond Device

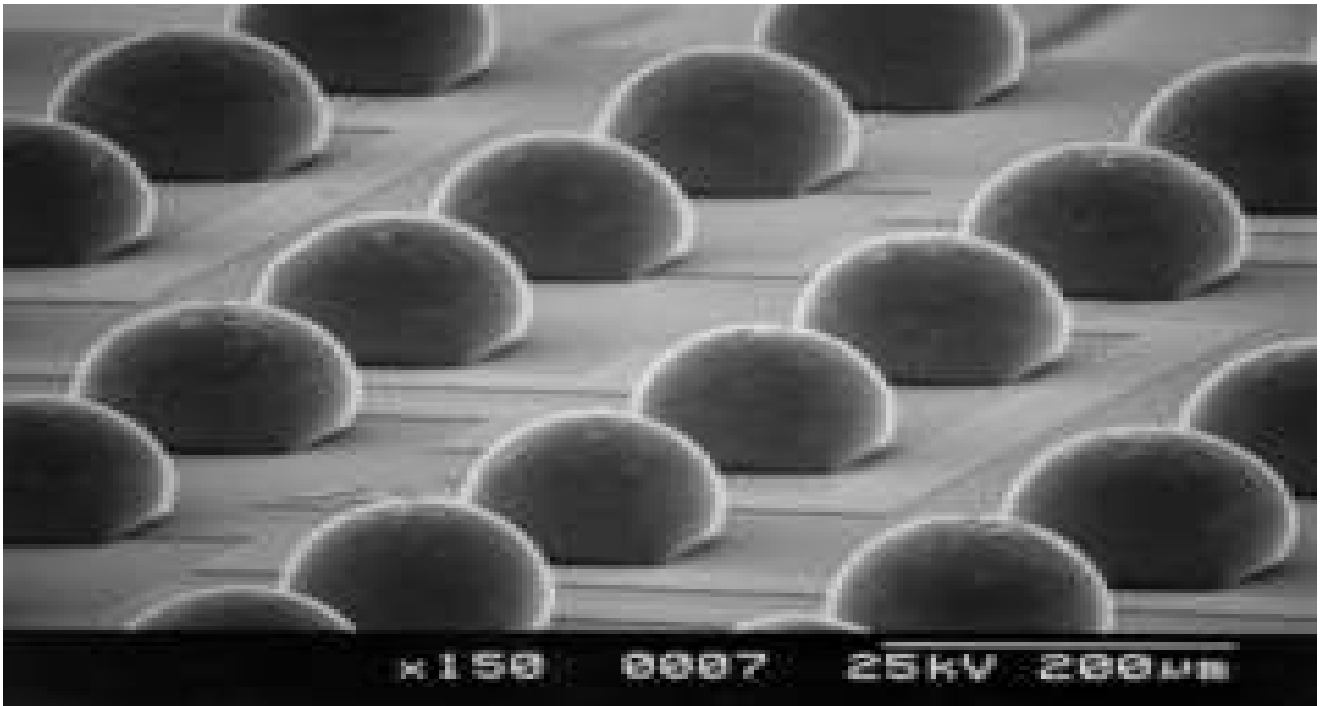


Figure 2 Flip Chip Device

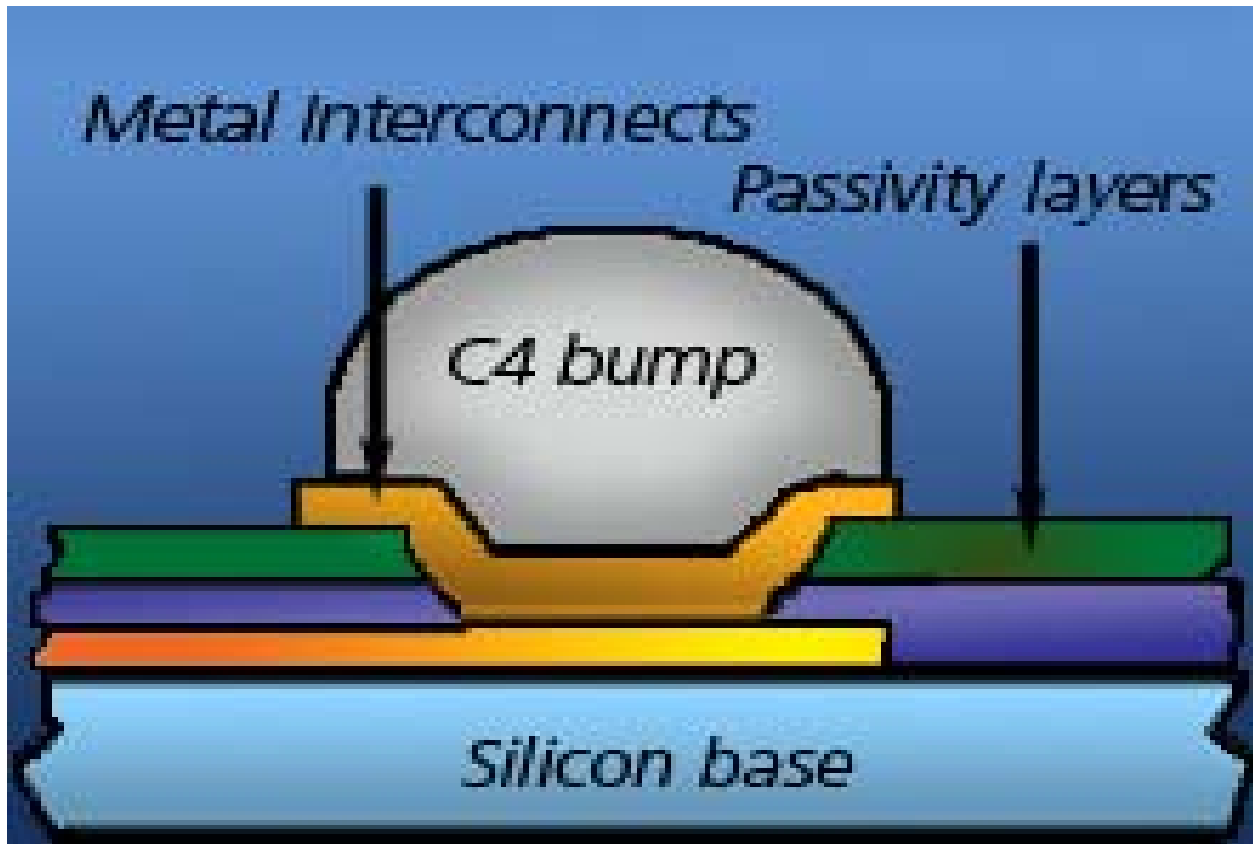


Figure 3 Flip Chip Bump Construction

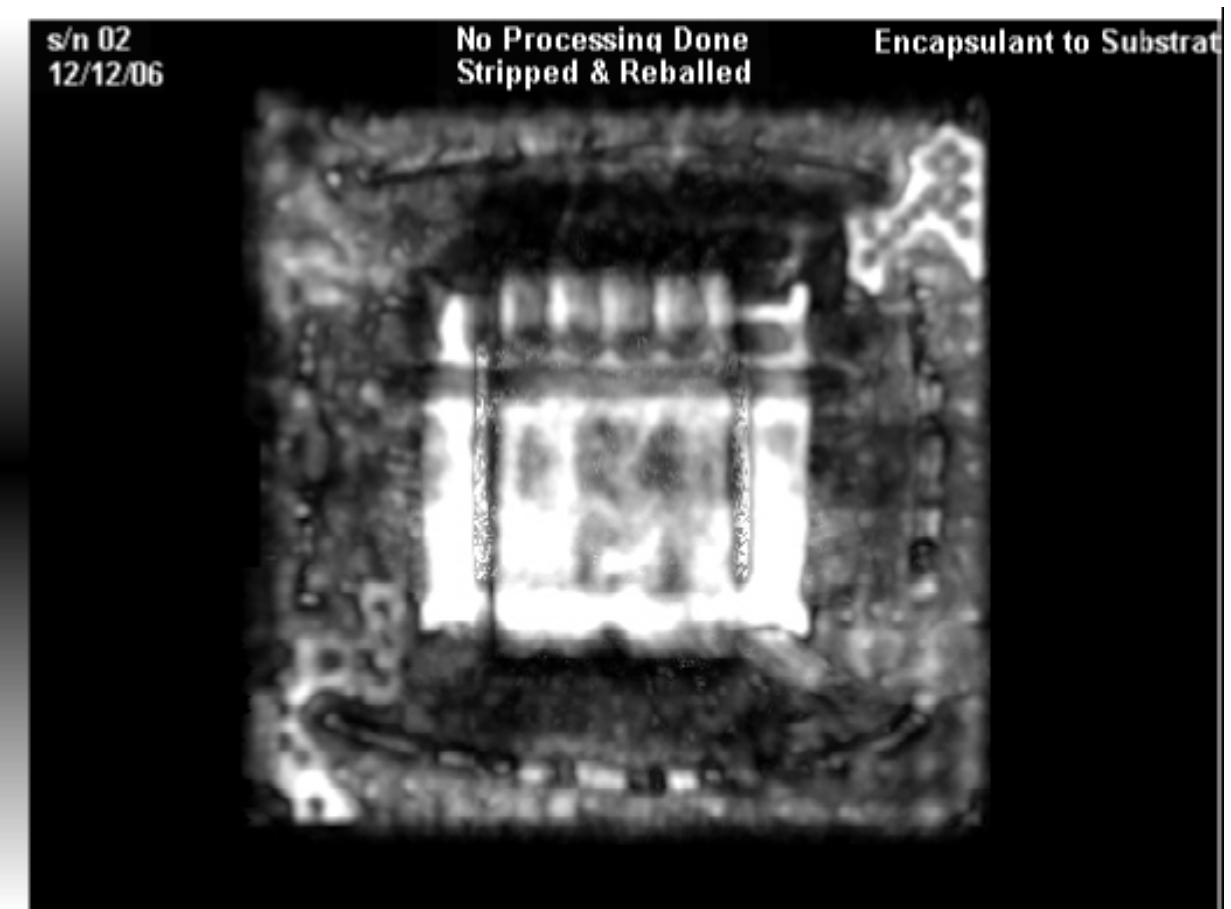
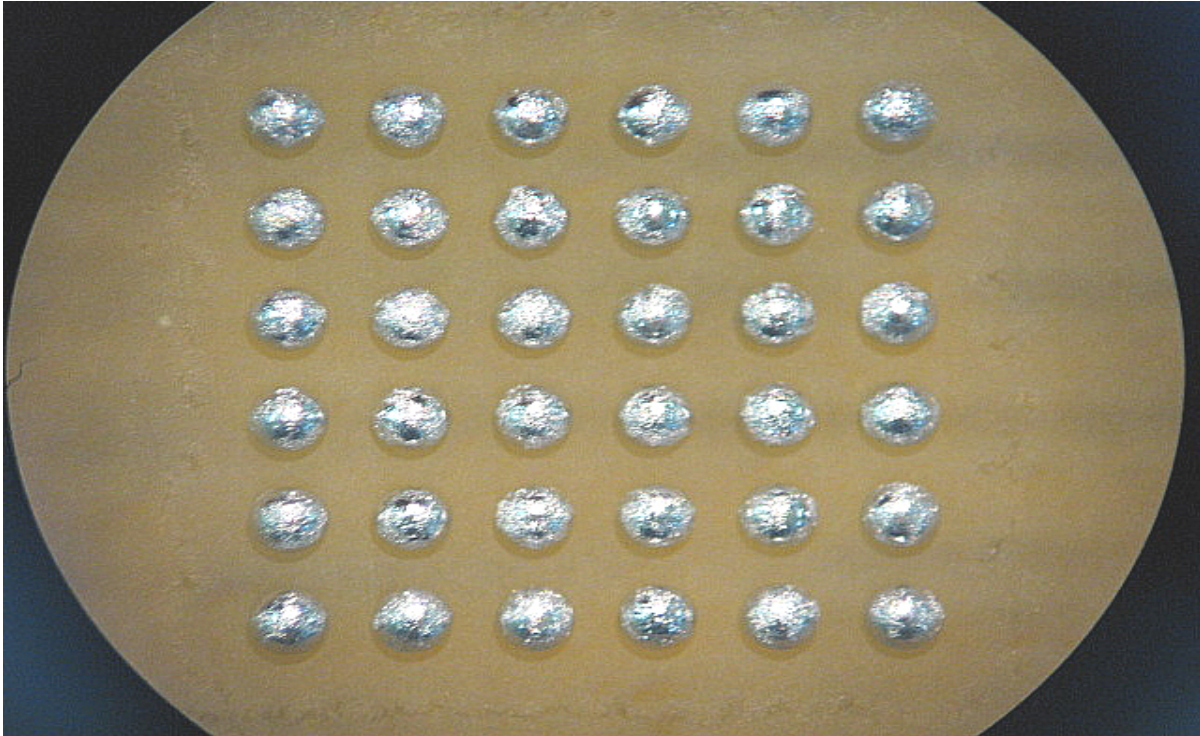


Figure 4 Encapsulation to Substrate

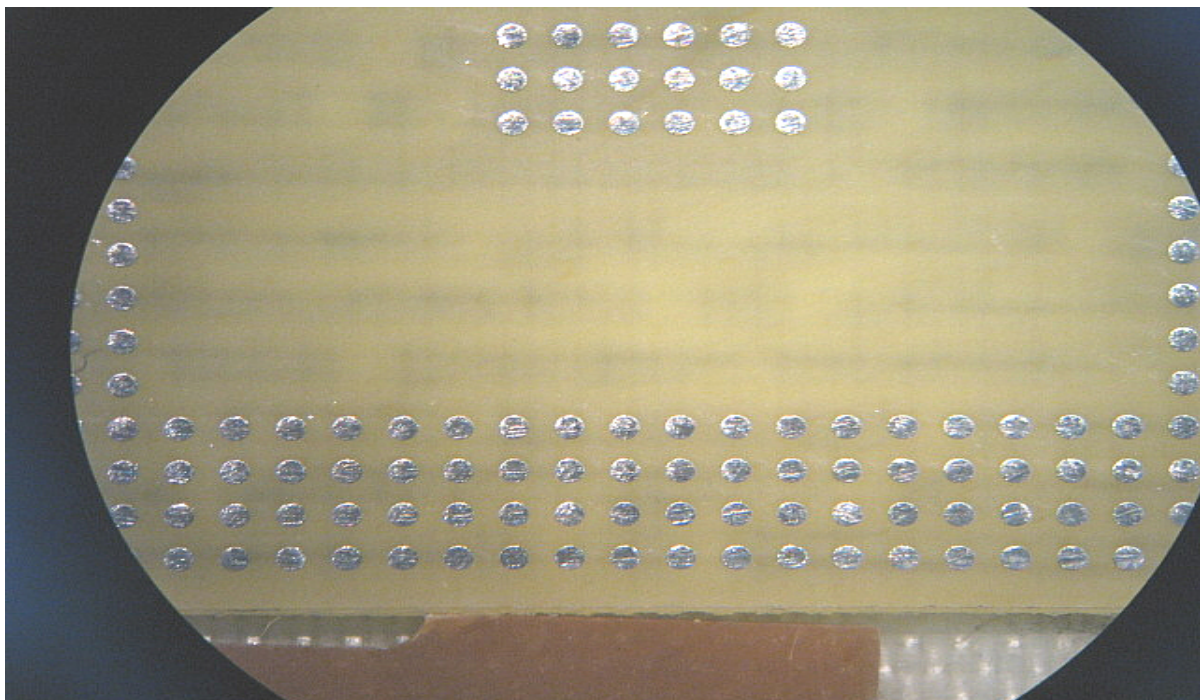
**Thermal Exposure Control by controlling the Profile Features:**

- Ramp up rate
- Preheat temperature
- Peak temperature range
- Cool down rate
- Do not approach the specific Pb free alloy re-flow temperature.

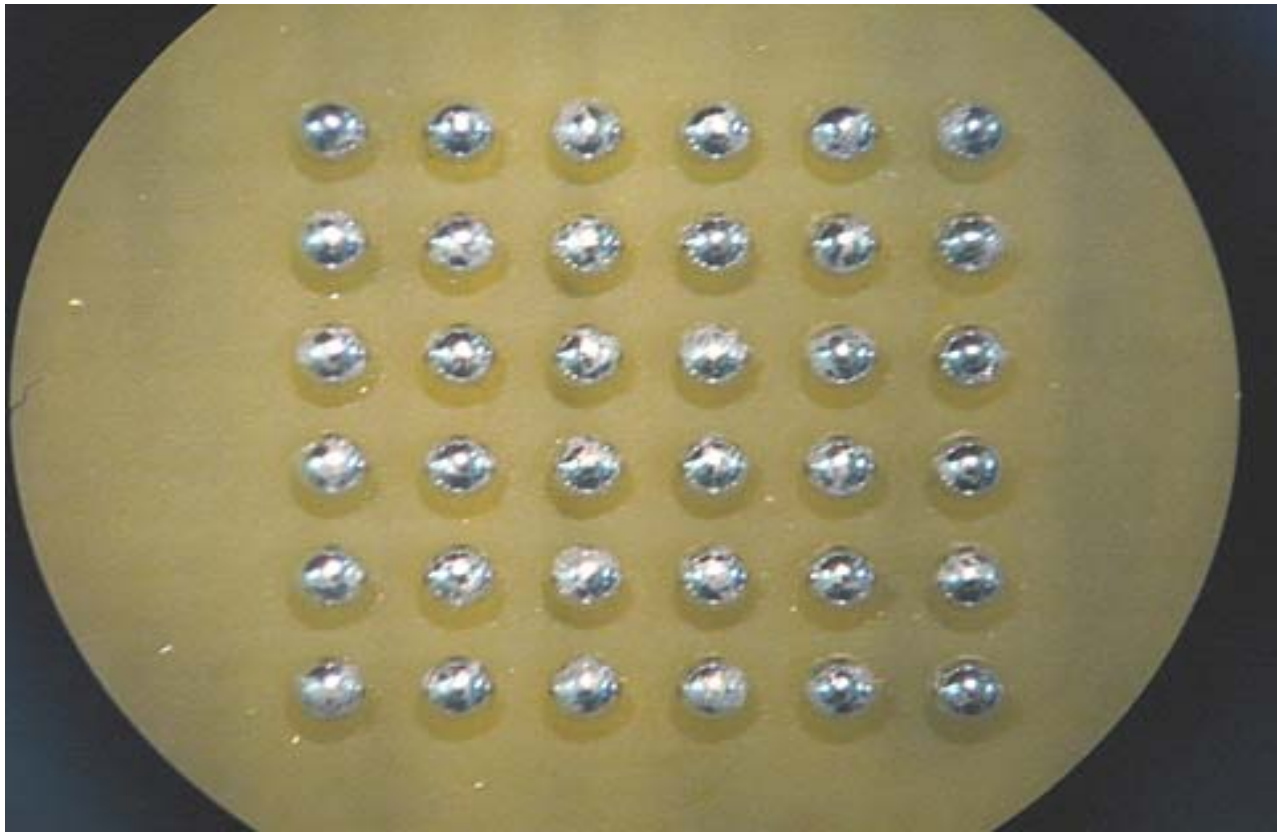
**Intermetallic Interaction (Package to PWB Interface)**



**Figure 5 BGA with Pb Free Spheres**



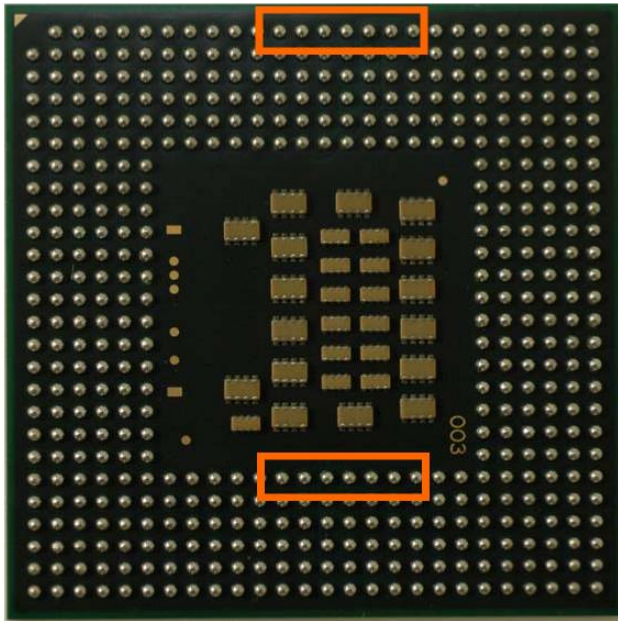
**Figure 5 BGA with Pb Free Stripped Spheres**



**Figure 6 Attached Spheres**

**Tests performed to validate the solder ball attachment process**

- Shear testing
- C-Sam for Substrate delamination
- SEM Inspection of Solder Joint



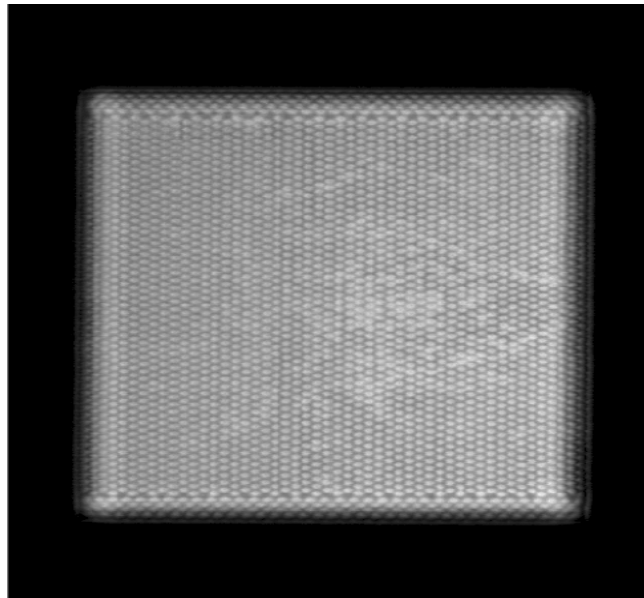
**Figure 7 Shear Test (0.6mm Sphere)**



**Shear Strength Data**

**Table 2**

Ball Dia.	Test #	Force (g)	Description	Platform	Load Cell
0.3mm	1	241.58	Pass	Dage 4000	BS5Kg
0.3mm	2	237.18	Pass	Dage 4000	BS5Kg
0.3mm	3	230.50	Pass	Dage 4000	BS5Kg
0.3mm	4	238.53	Pass	Dage 4000	BS5Kg
0.3mm	5	235.77	Pass	Dage 4000	BS5Kg
0.6mm	1	1538	Pass	Dage 2400	BS2Kg
0.6mm	2	1480	Pass	Dage 2400	BS2Kg
0.6mm	3	1701	Pass	Dage 2400	BS2Kg
0.6mm	4	1591	Pass	Dage 2400	BS2Kg
0.6mm	5	1427	Pass	Dage 2400	BS2Kg



**Figure 8 FC die/underfil interface showing no delaminations from Substrate**

Twenty four (24) of the forty one (41) packages were subjected to CSAM analysis. The purpose of this evaluation is to make sure that multiple re-flow temperature profiles subjected to the packages during the lead free ball removal and the subsequent re-balling process did not initiate or cause underfil delaminations at the flip chip die interface.

This type of evaluation is normally recommended to be performed when Flip Chip (FC) packages are exposed to temperature excursion or profiles. Underfil delaminations can either failures or compromise the integrity and reliability of the package solder bump interconnects.



Figure 9 Example of an Acoustic Transducer Response with response gates at special package interfaces of interest

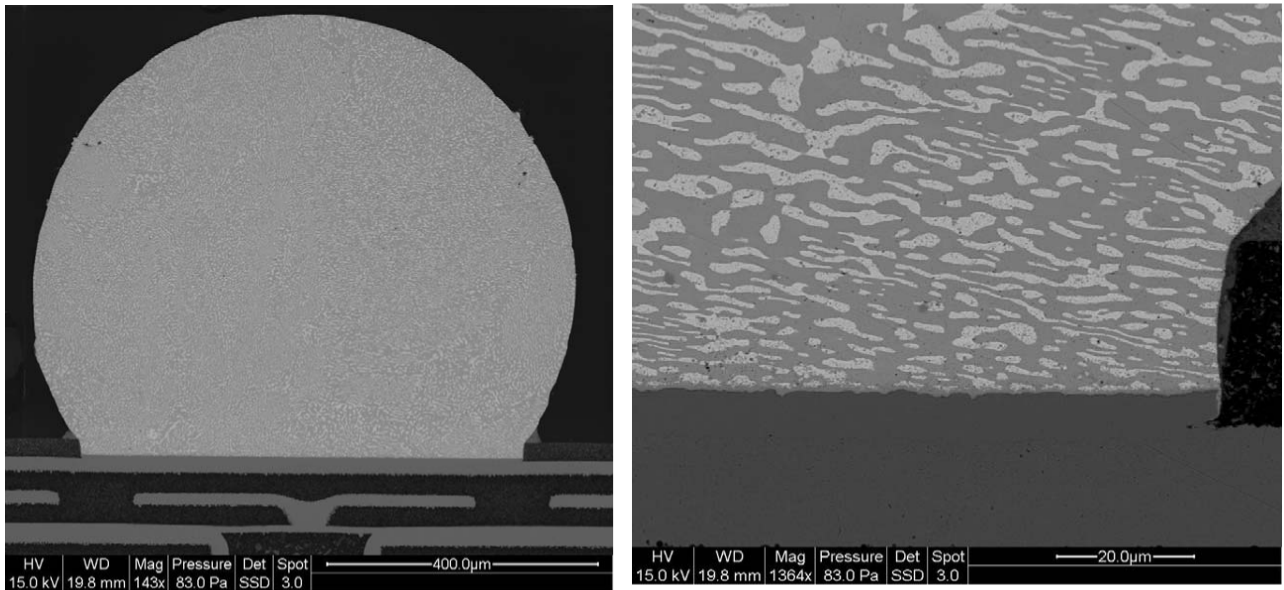
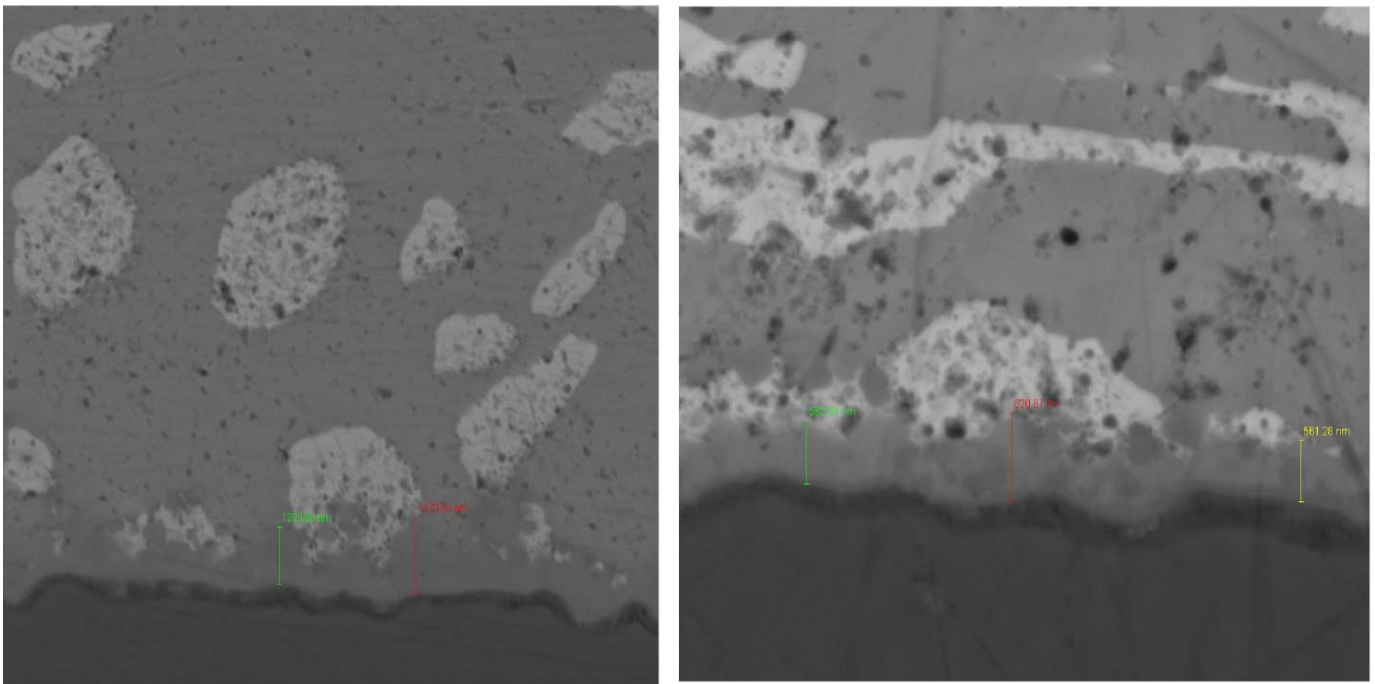


Figure 10 SEM Inspection of Solder Joint





**Figure 11 Intermetallic Region**

#### **Analytic Conclusion**

- X-ray inspection showed no voiding
- Cross sections showed good metallurgical ball attachment
- CSAM analysis - no FC/underfil interface delamination
- Extensive solder ball shear performed showed good BGA to substrate solder joints with tight shear force range all above the industry acceptable level. The shear failure was always in the bulk solder indicating strong solder joint to the substrate pads.

#### **Summary**

Proprietary processes have been developed for stripping Pb-free BGA's mitigating inter-metallic layers & attaching Sn/Pb spheres utilizing controlled thermal profiles. Similar reliable processes have been demonstrated for the conversion of Pb free parts such as QFP, TSOP, PLCC, resistors & other types to Sn/Pb. This conversion option to Sn/Pb gives device manufacturers and high reliability users real alternatives on how to deal with the availability of Sn/Pb parts & granting the OEMs their freedom back to choose and be able to put the Sn/Pb proven reliability back in their builds.

# ***Bridging Supply Chain Gap for Exempt High-Reliability OEM's***

***Presented by: Hal Rotchadl***

# Industry Experience

- Counterfeit Detection
- Electrical & Upscreen Test
- Wafer Probe
- Failure Analysis
- IC Programming/Laser Mark
- Lead Straightening
- 3D & Laser Scan
- BGA Reball & Ball Attach
- Sn/Pb & Pb Free Conversion
- Hot Solder Dip and Retinning
- Fine and Gross Leak Testing
- Tape & Reel
- Bake & Dry Pack
- Trim & Form

# Supply Chain Issues

- Consumption, in terms of unit volume for Sn/Pb, is small compared to the volume going into the builds of Pb free consumer and commercial product
- Many device manufacturers are discontinuing the Sn/Pb option on many part numbers (P/N) when unit volumes fall below a certain threshold

# High Reliability OEM's Concerns

- Components are being transitioned to obsolete and legacy parts outside the control of the OEMs and at a rapid pace
- Life cycle for a military product generally takes over two years for the design and initial deployment, followed by a production life cycle of over 10 years and a repair/warranty cycle of 20 plus years
- A redesign to include an alternate part number is no easy task due to redesign review, validation and reliability testing

# High Reliability OEM's Concerns

- Some manufacturers are not changing P/Ns once the Sn/Pb is obsolete. The end result too often is mixed reels of RoHS and non-RoHS product
- Reliability concerns associated with Pb-free components. Reflow profiles, thermal stress, MSL, tin whiskers, tin pests, brittleness, voids and thermal mismatch,...
- Exempt OEMs are many times left with only one choice and that is Pb-free components

# Bridging the Gap

- Proprietary processes have been developed for stripping Pb-free BGA's mitigating inter-metallic layers & attaching Sn/Pb spheres utilizing controlled thermal profiles
- Reliable processes have been demonstrated for the conversion of Pb free parts such as QFP, TSOP, PLCC, resistors & other types to Sn/Pb
- This conversion option to Sn/Pb gives device manufacturers and high reliability users real alternatives on how to deal with the availability of Sn/Pb parts

# Presentation Focus – BGA'S

The conversion process for BGA's is more complex to control compared to other device types & is the focus for this presentation



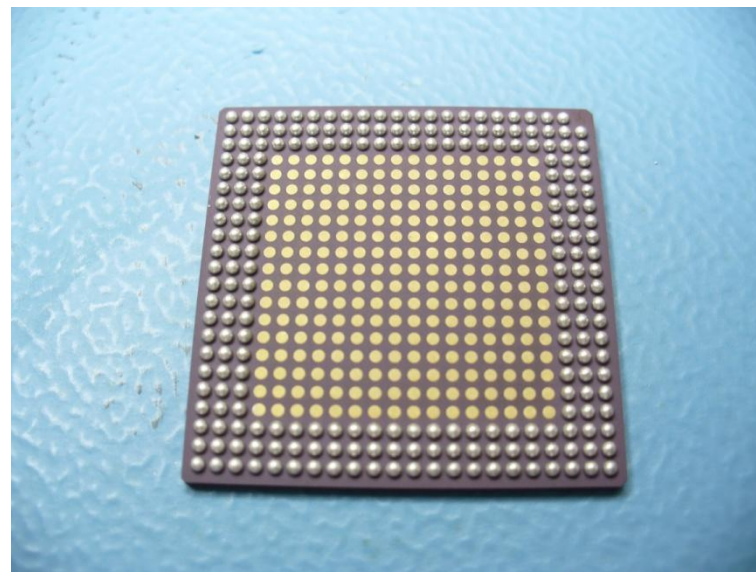
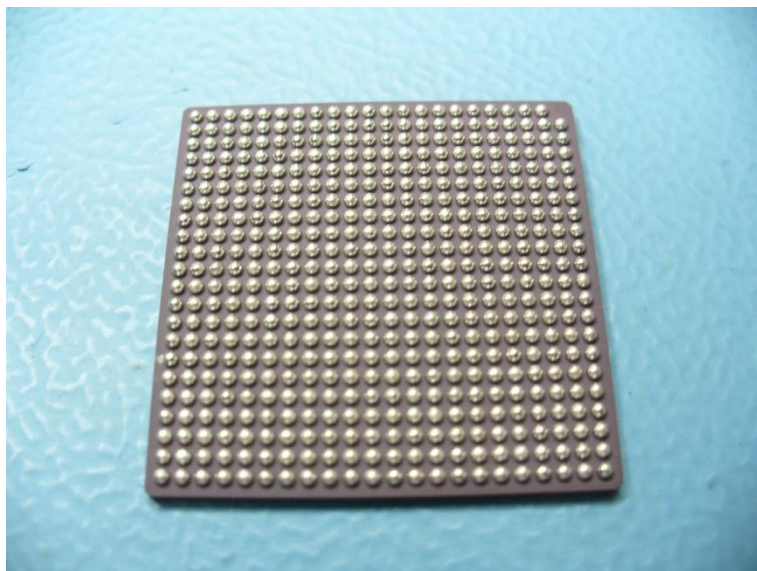
# OEM Statement (Lead-free/BGA Reball)

“Lead-free” RoHS initiatives to remove lead from electronic equipment have resulted in challenges for manufacturers. Due to known reliability issues with tin solder alloys (e.g., tin whiskers & voids), companies building high-reliability equipment **must approach lead-free soldering with care**. A technique being considered for BGA devices is that of re-balling, that is, **removal of lead-free solder and replacement by tin-lead spheres**. The question is whether this procedure is advisable or if there are other options.”

# Pb Free Alloy Test Plan

- Overview:
  - Perform a limited sample size thermal cycle “screening” test covering/comparing multiple different alloys
  - Use existing test board design and component (Acceleration Factors Test)
  - Single thermal cycle (0-100C, 30 minute dwell)
  - Single soldering alloy: SAC 305

# Pb Free Alloy - Test Devices



# Pb Free Alloy Test - Compositions

Packages of 9 different solder alloys were received

SAC405,

SnAg,

Sn100C,

Sn1.2Ag0.5Cu0.005Ni,

SAC205+Ni,

SAC310 +1%Cu,

SAC105,

SAC108,

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Each Alloys came into 2 different groups

- 1) Fully populated components
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# Pb Free Alloy Test - Early Results

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SAC105D	88%	removed from chamber
SAC108F	63%	
SAC108D	88%	removed from chamber
SACXF	81%	removed from chamber
SACXD	100%	removed from chamber

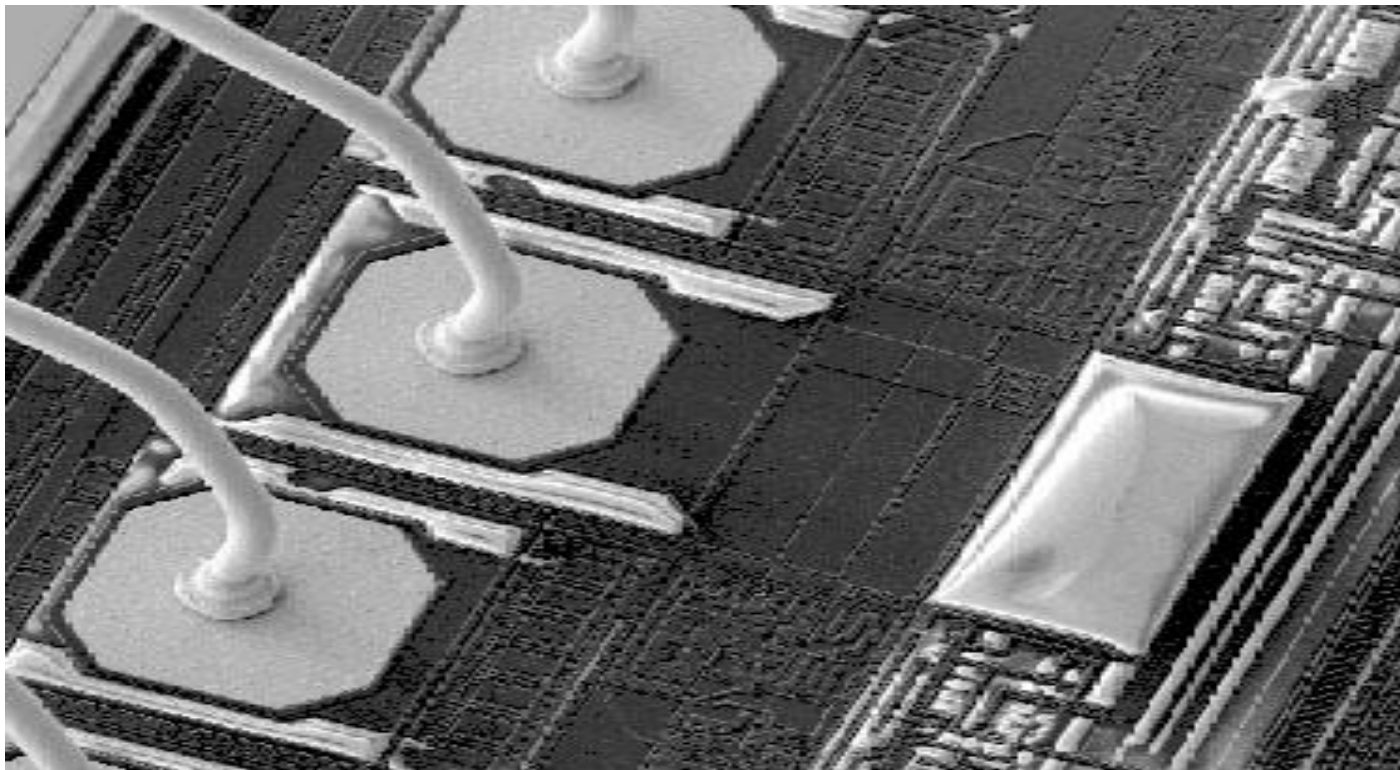
# BGA Re-ball Chronology

- I. Device removal and refurbishment  
For revisions, board rework, PWBA failures, etc
  
- II. Sn/Pb to Pb Free Conversion  
For legacy products requiring RoHS Compliance
  
- III. Pb Free to Sn/Pb Conversion  
For High reliability products with RoHS exemptions
  
- IV. Sphere attach to Land Grid Array (LGA)  
For High reliability products with RoHS exemptions

# High Reliability Industry Concerns

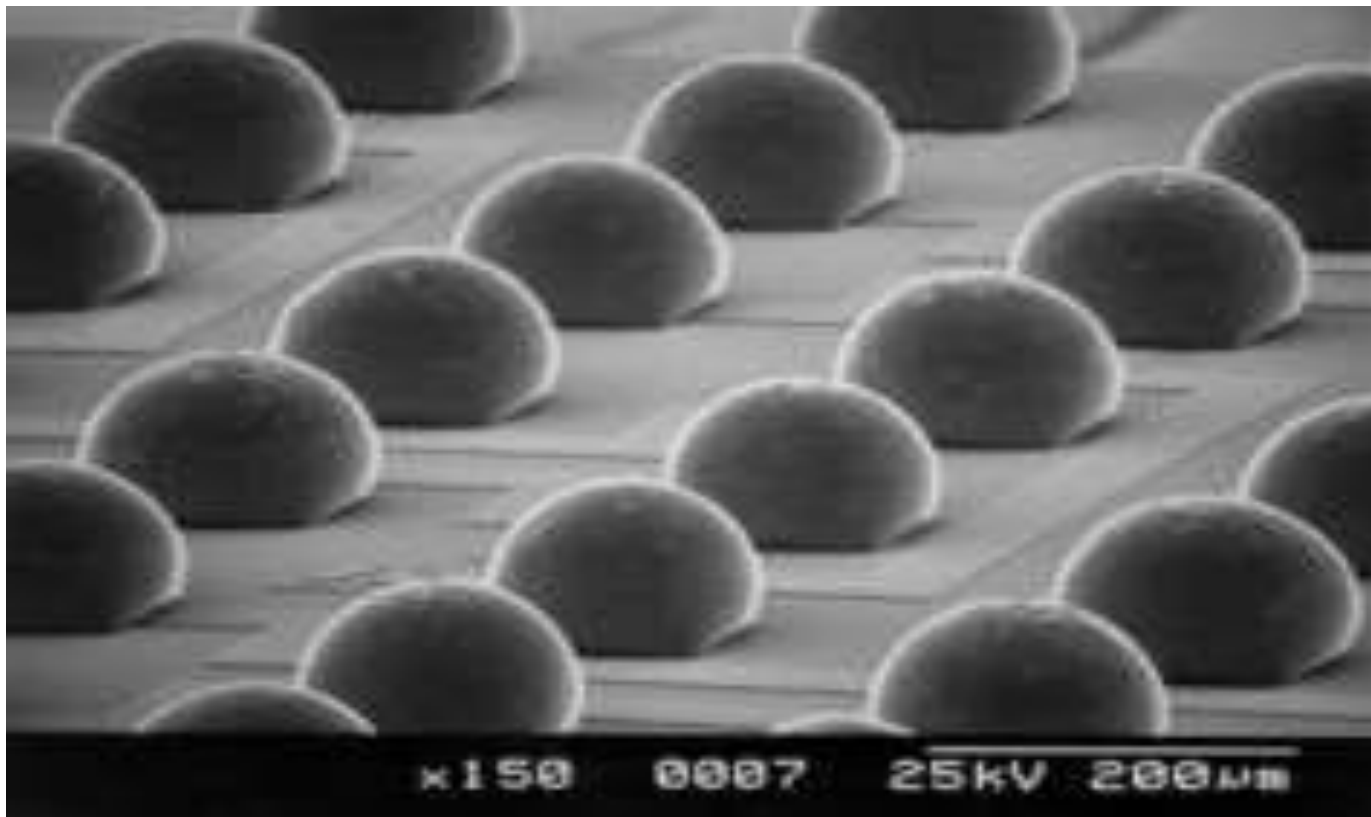
- **Additional Thermal Exposure**  
Silicon to Package Interface
- **Intermetallic Interaction**  
Package to PWB Interface

# Wirebond Device

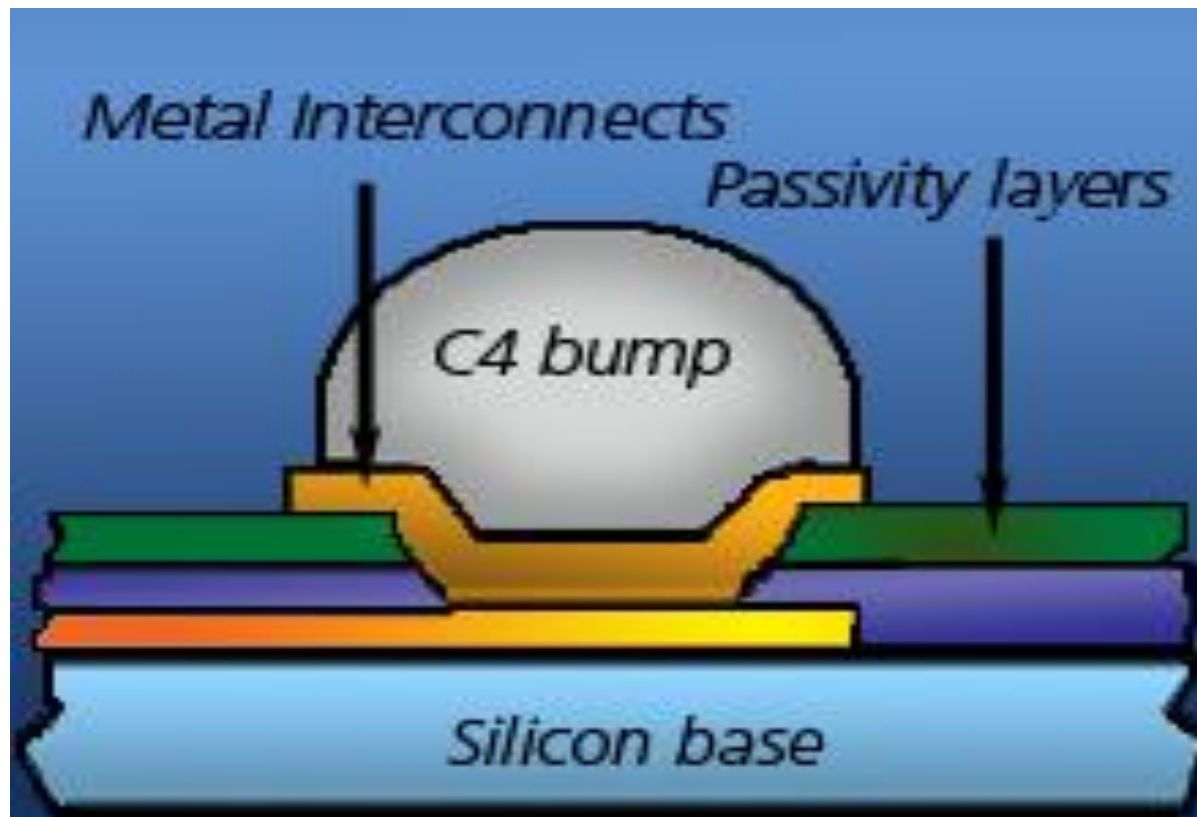




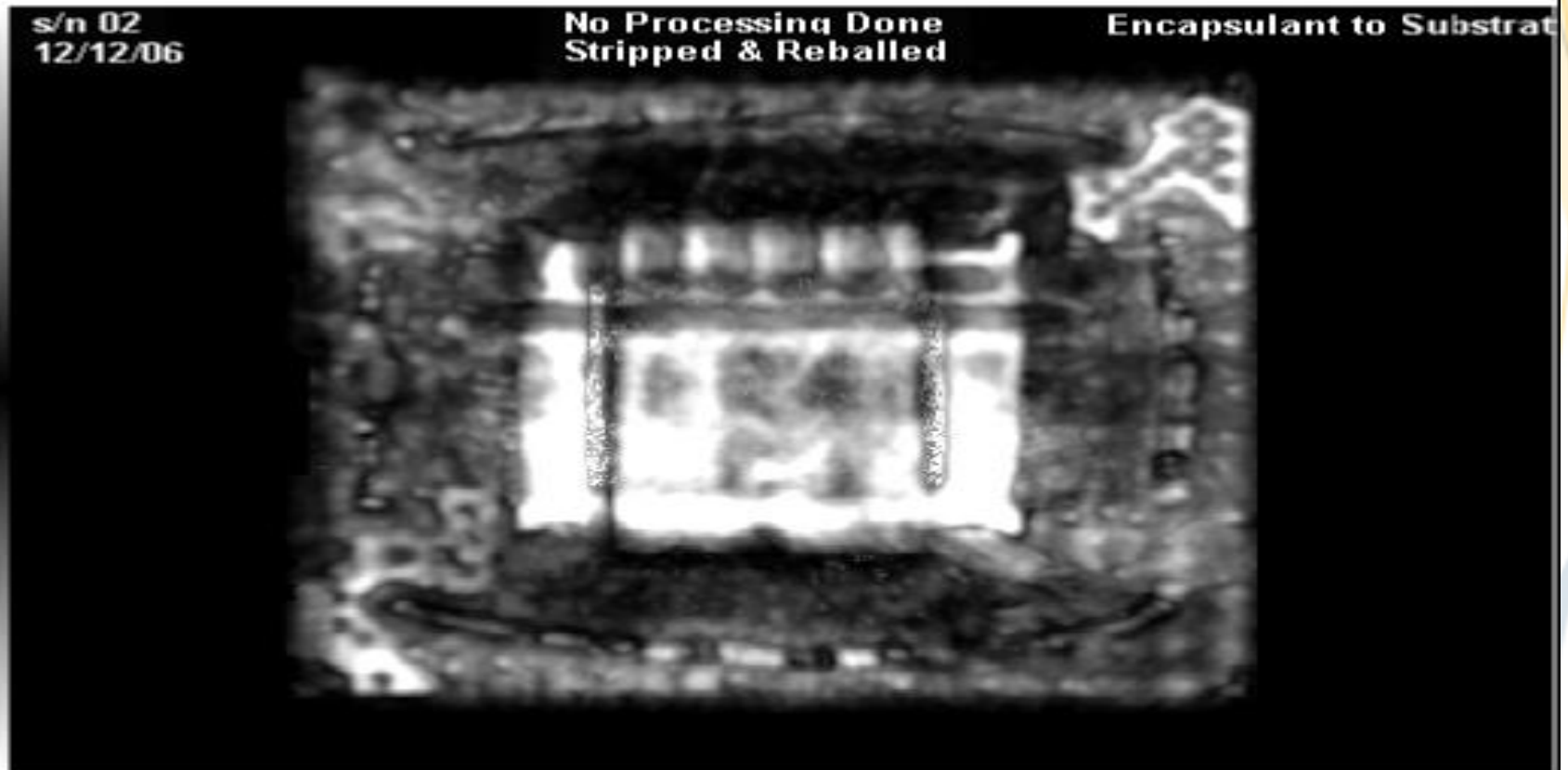
# Flip Chip Device



# Flip Chip Bump Construction



# Encapsulant to Substrate



# Thermal Exposure Control

## Profile Feature

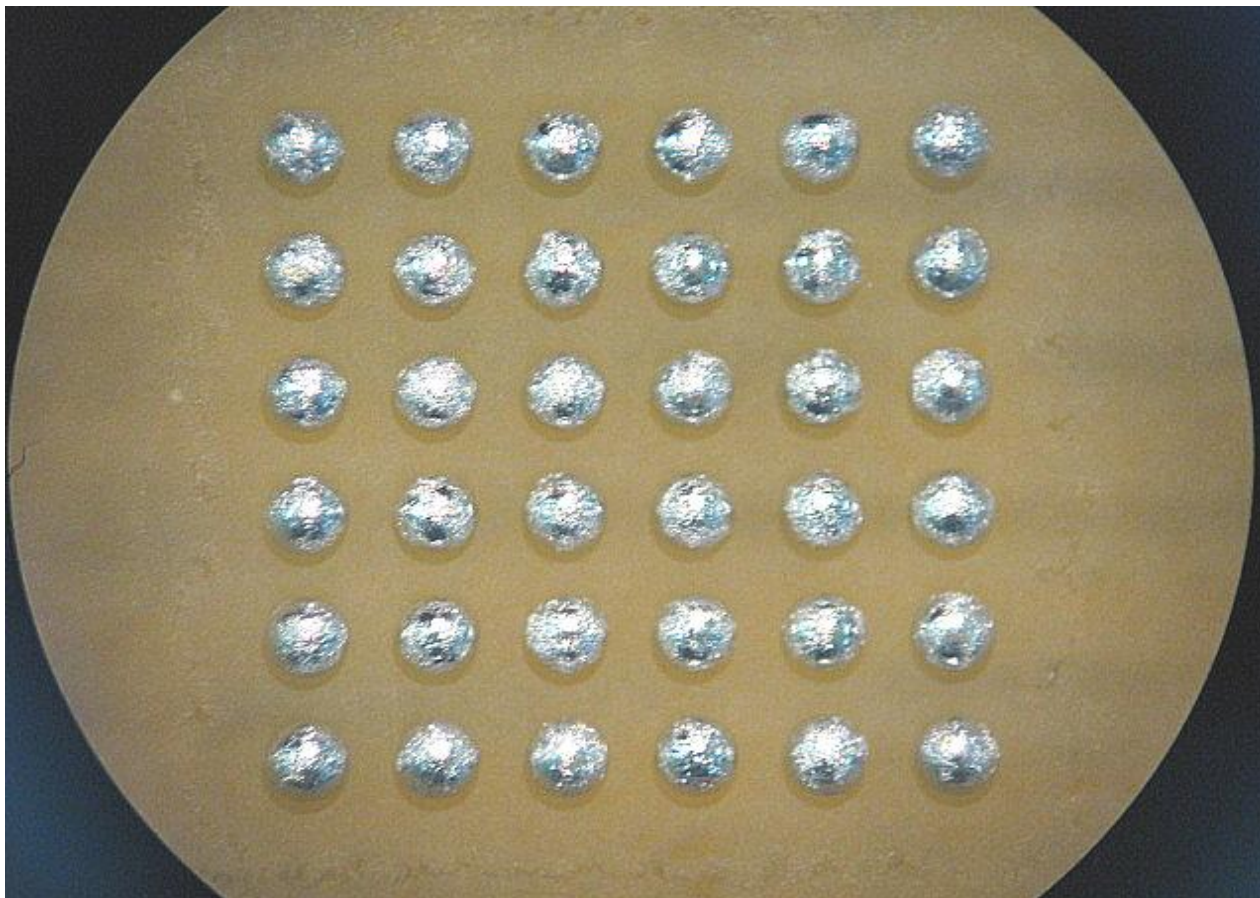
- Ramp up rate
- Preheat temperature
- Peak temperature range
- Cool down rate
- Do not approach the specific Pb free alloy re-flow temperature.

\* Profile feature data is device specific

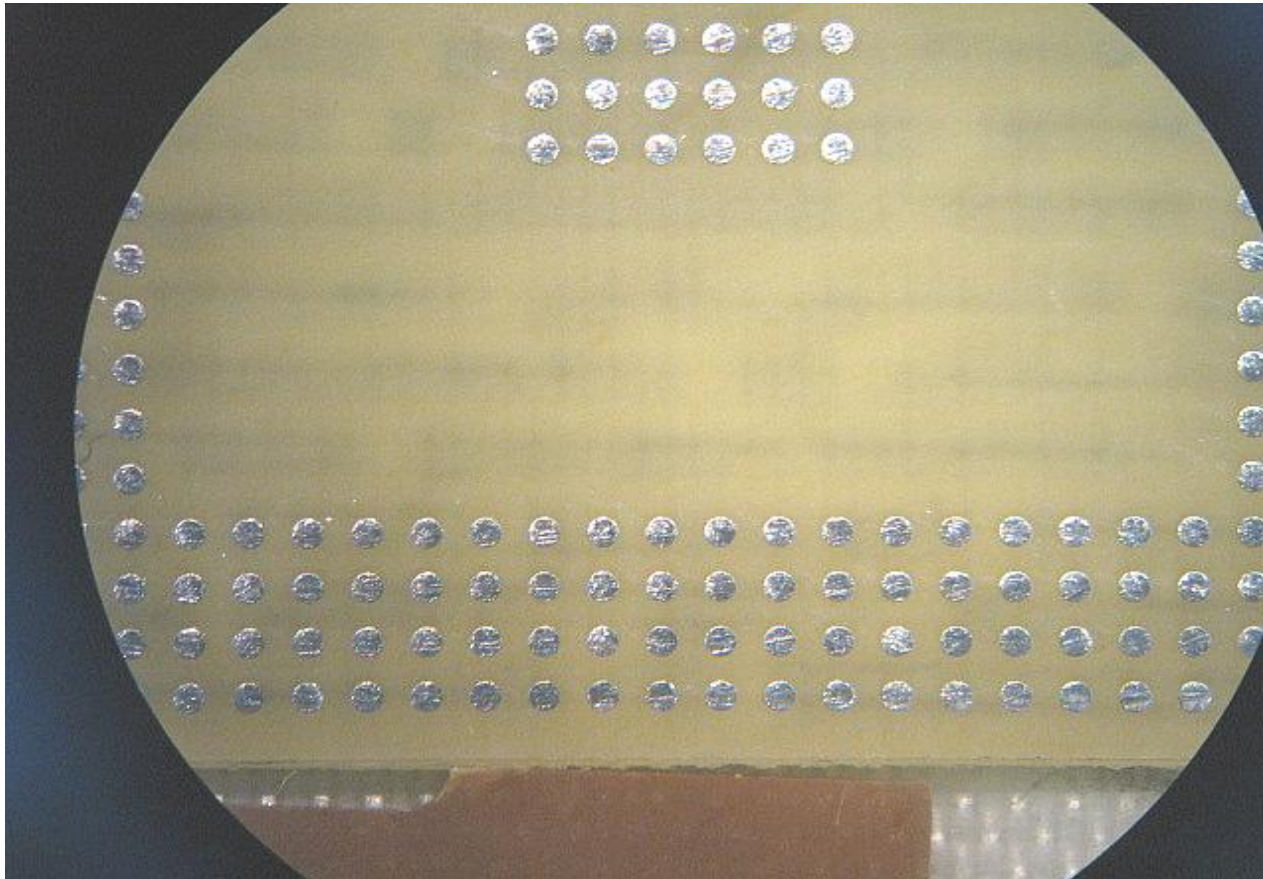
# High Reliability Industry Concerns

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- **Intermetallic Interaction**  
Package to PWB Interface

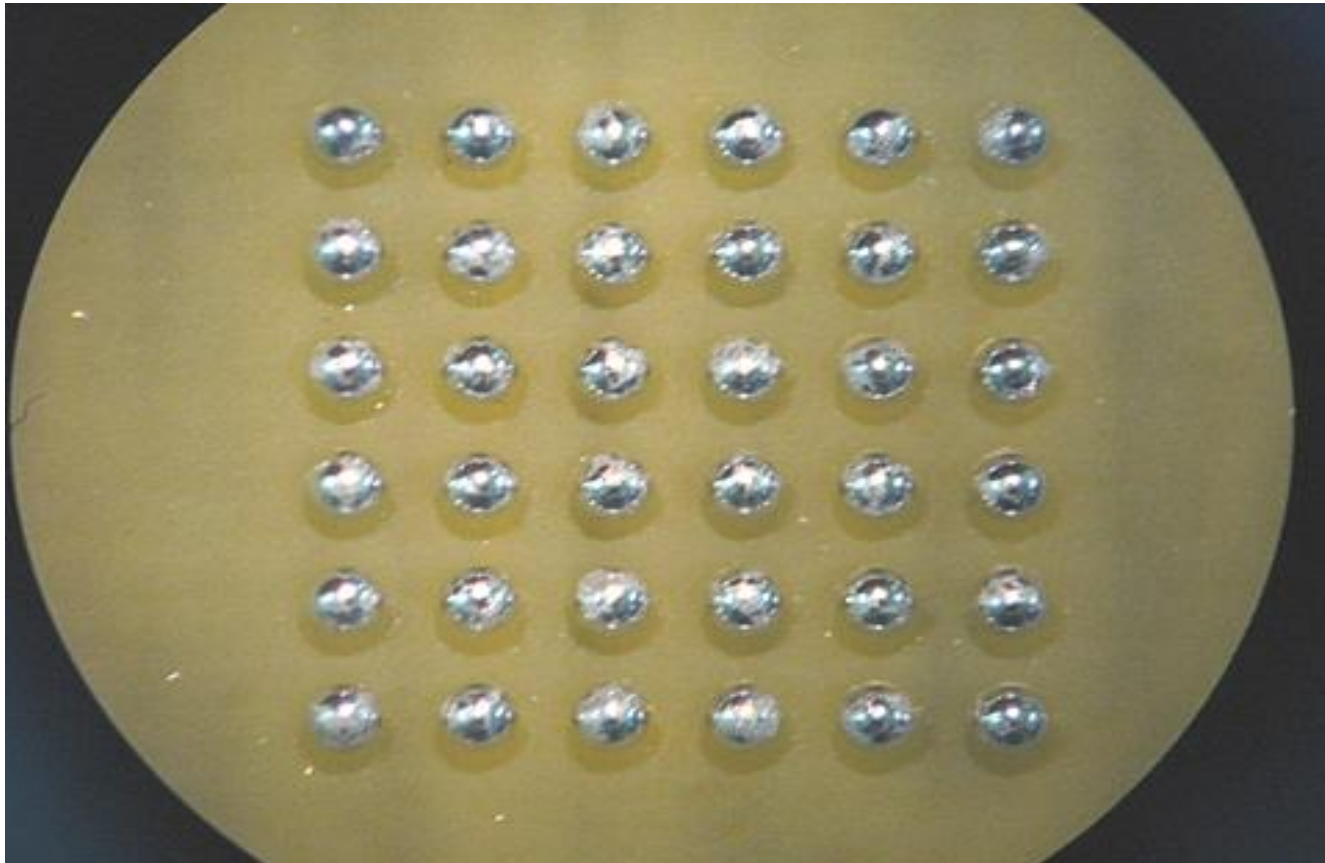
# Pb Free Spheres



# Strip Process



# Sphere Attach





# Flux/Paste & Ball Attach



# Reflow & Clean



# 3D Ball Scan, Tape & Reel



# Ball Attach Integrity Testing

Tests performed to validate the solder ball attachment process

- Shear testing
- C-Sam for Substrate delamination
- SEM Inspection of Solder Joint

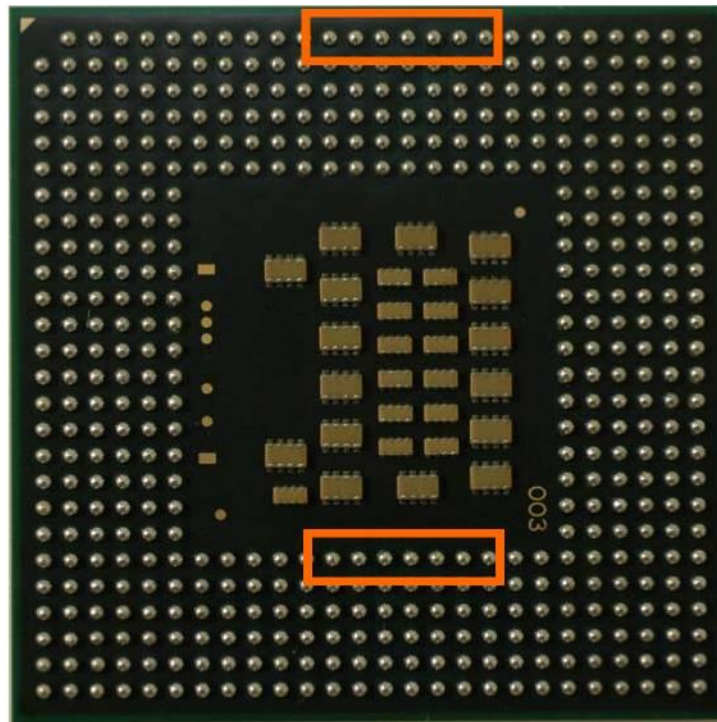
# Electrical Test Capabilities



# Pb Free to Eutectic – Case Study

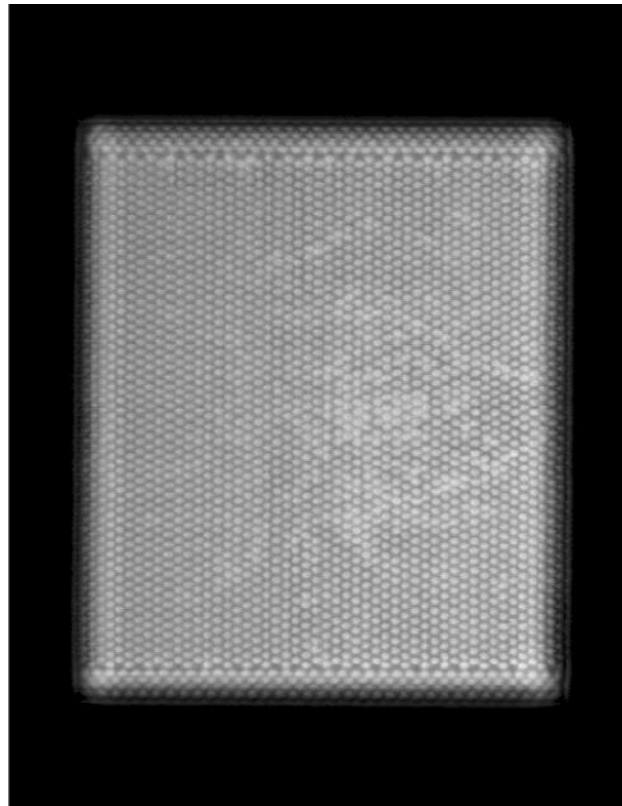
- The following series of photos reflect results of a specific proprietary process.
- The requirement was to convert a series of devices from a SAC405 interconnect structure to a eutectic structure.
- The devices were sent by the customer to an independent laboratory for evaluation.

# Intel U2500 Duo-Core Processor



# C-SAM of Flip Chip Bumps

**FC die/underfil interface showing no delaminations from Substrate**





# Acoustic Analysis

Example of an **Acoustic Transducer Response** with response gates at special package interfaces of interest.

Twenty four (24) of the forty one (41) packages were subjected to CSAM analysis. The purpose of this evaluation is to make sure that multiple re-flow temperature profiles subjected to the packages during the lead free ball removal and the subsequent re-balling process did not initiate or cause underfil delaminations at the flip chip die interface.

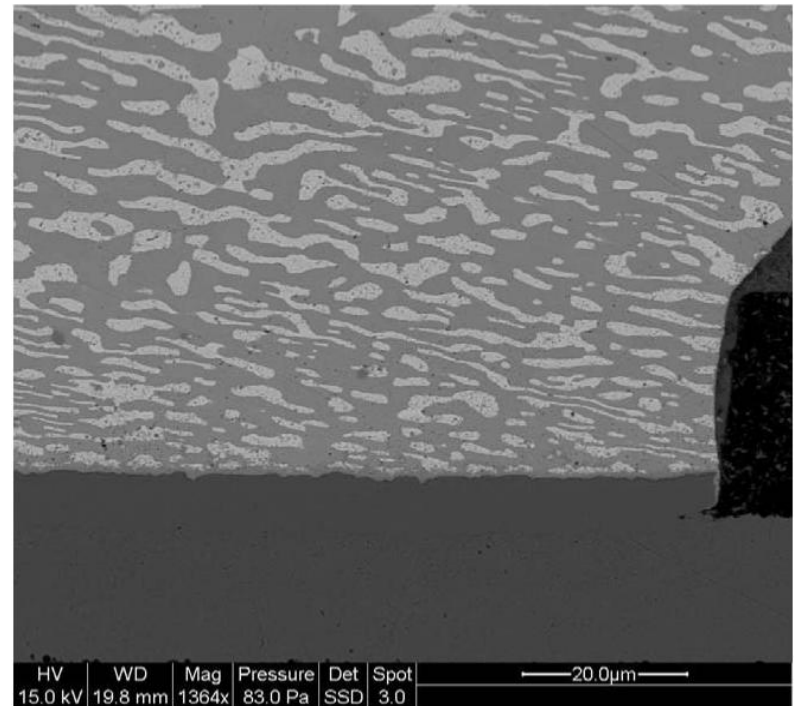
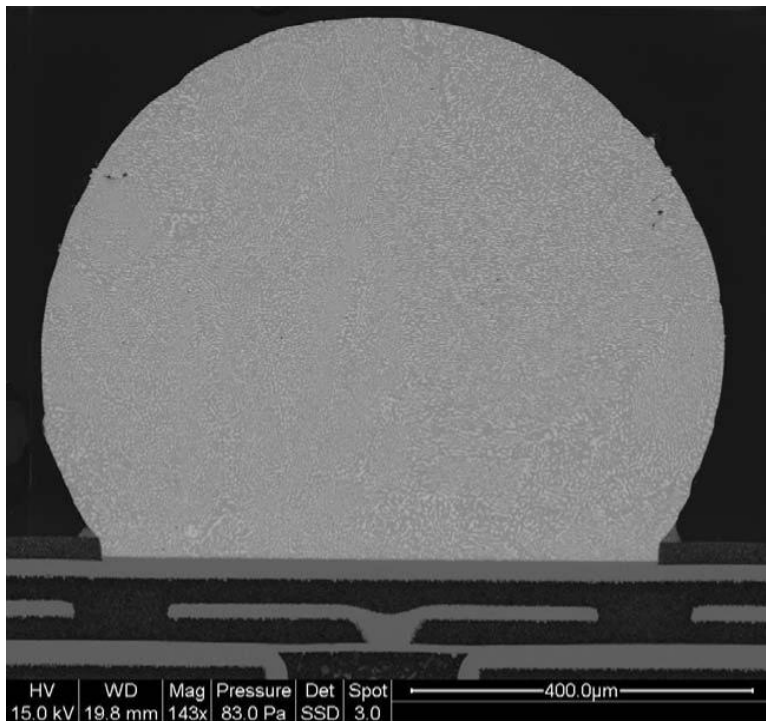
This type of evaluation is normally recommended to be performed when Flip Chip (FC) packages are exposed to temperature excursion or profiles. Underfil delaminations can either failures or compromise the integrity and reliability of the package solder bump interconnects.

# Acoustic Analysis Chart

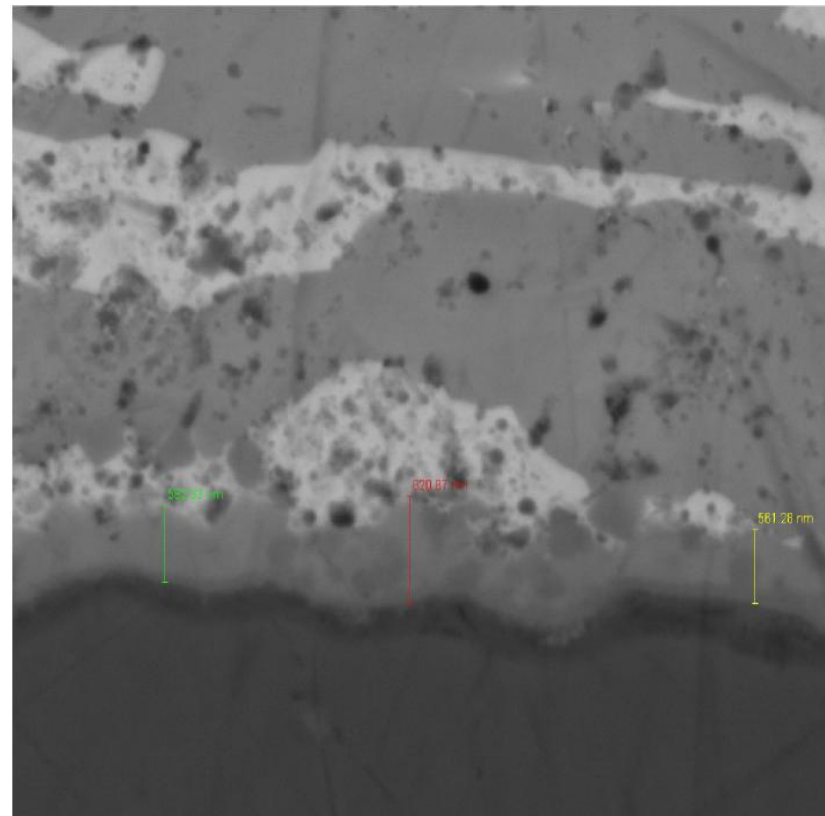
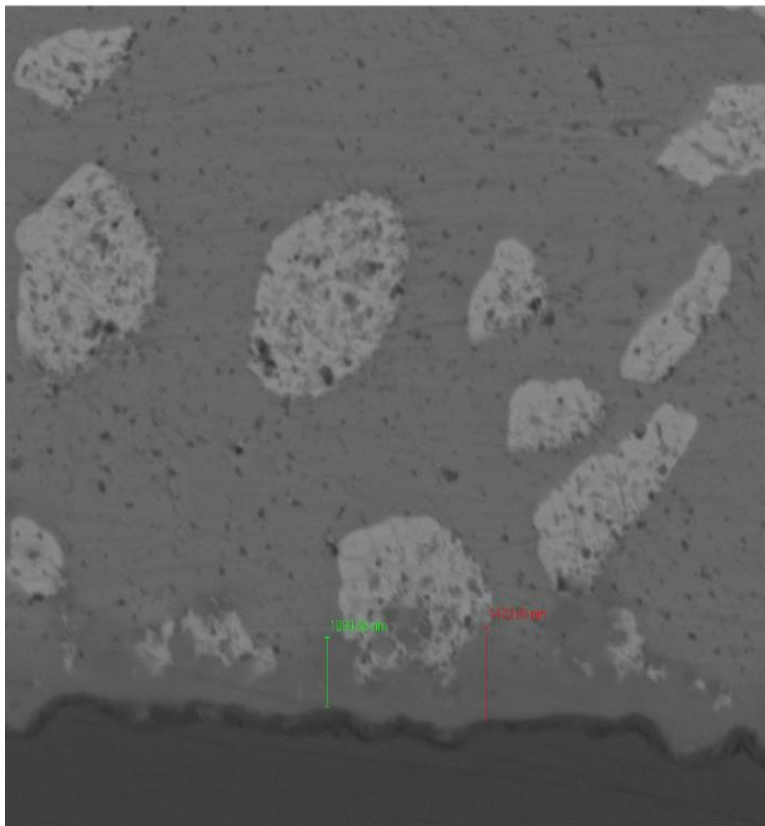
## Illustration of Acoustic Transducer Response Chart



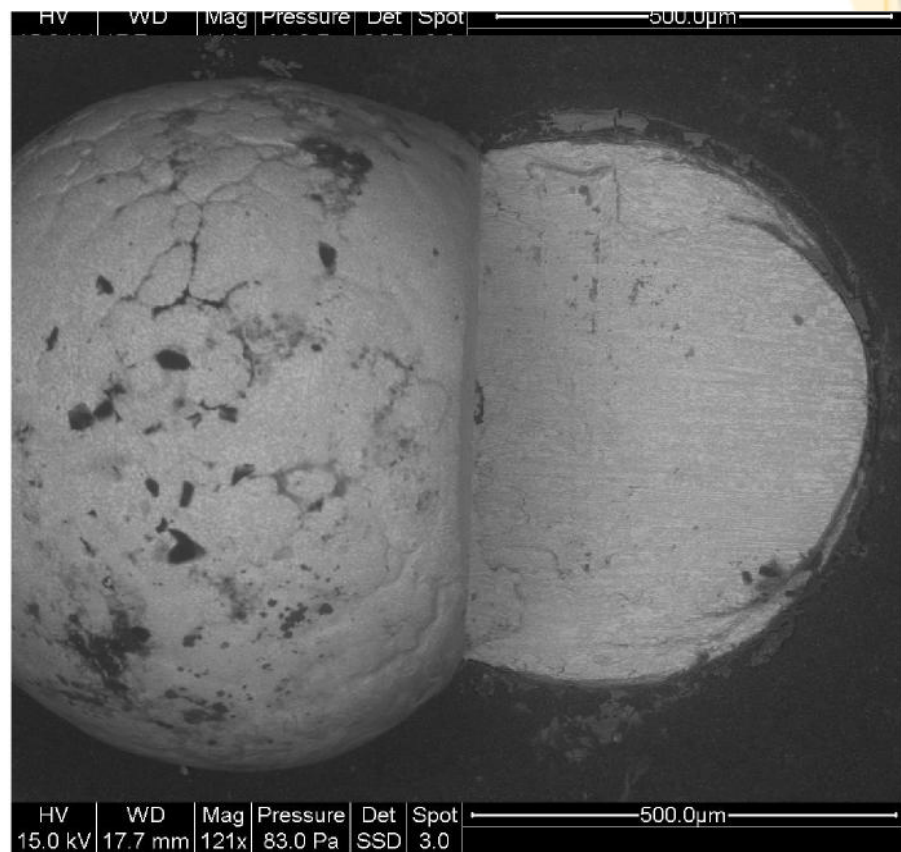
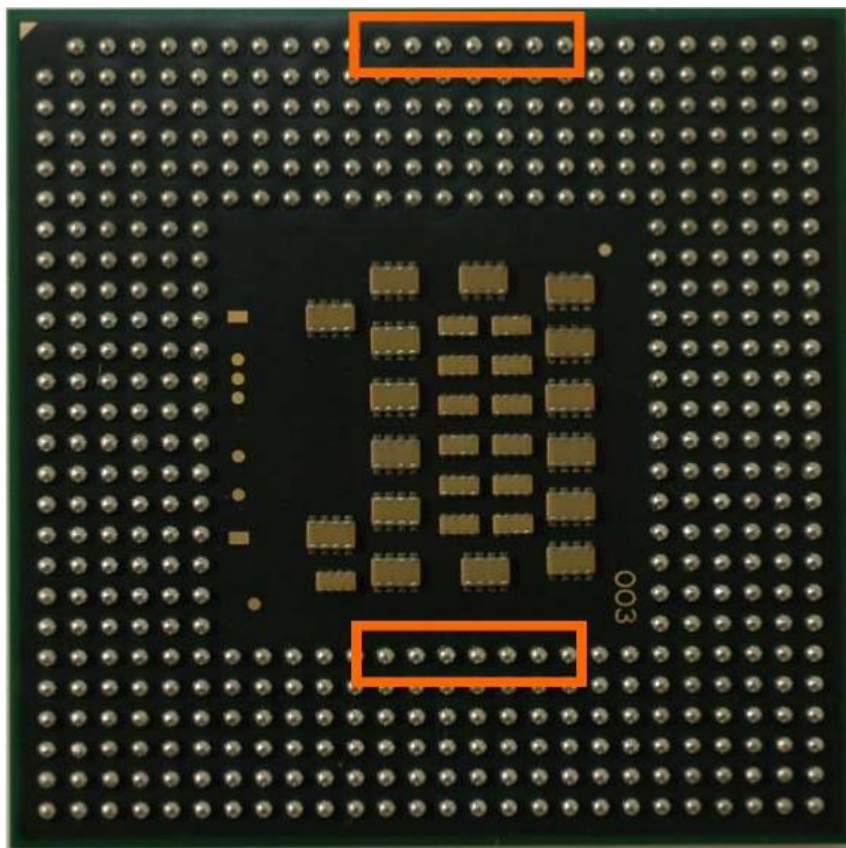
# Solder Joint



# Intermetallic Region



# 0.6mm Ball Shear



# Shear Strength Data

Ball Dia.	Test #	Force (g)	Description	Platform	Load Cell
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0.3mm	2	237.18	Pass	Dage 4000	BS5Kg
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0.3mm	4	238.53	Pass	Dage 4000	BS5Kg
0.3mm	5	235.77	Pass	Dage 4000	BS5Kg
<b>0.6mm</b>	<b>1</b>	<b>1538</b>	<b>Pass</b>	<b>Dage 2400</b>	<b>BS2Kg</b>
<b>0.6mm</b>	<b>2</b>	<b>1480</b>	<b>Pass</b>	<b>Dage 2400</b>	<b>BS2Kg</b>
<b>0.6mm</b>	<b>3</b>	<b>1701</b>	<b>Pass</b>	<b>Dage 2400</b>	<b>BS2Kg</b>
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<b>0.6mm</b>	<b>5</b>	<b>1427</b>	<b>Pass</b>	<b>Dage 2400</b>	<b>BS2Kg</b>

# Analytical Conclusions

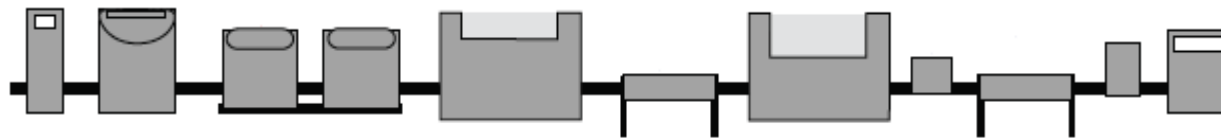
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- CSAM analysis - no FC/underfil interface delamination
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# Process Flow

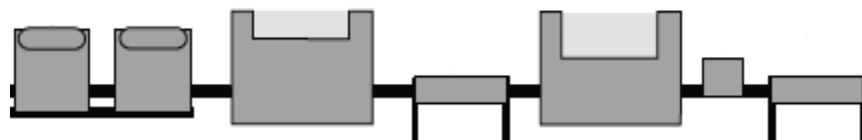
Wafer Dice – Sort – Die Attach / Flip Chip Attach – Underfil – Wire Bond – Encapsulate – Deflash



Inspect – Clean – Flux/Paste Print – Sphere Attach – Inspect – Re-flow – Inspect – Clean – Mark – Inspect – Scan



Singulate - Functional Test – Scan – Final Pack – Inspect - Ship





# High Reliability Device Plan

- Outsource Hi-Rel devices after de-flash process step for completion
- Produce off roadmap device in a Land Grid Array (LGA) format for marketplace.
- Make the device supplier part of the solution



# BGA Inquiry Contact Information

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[sales@premiers2.com](mailto:sales@premiers2.com)